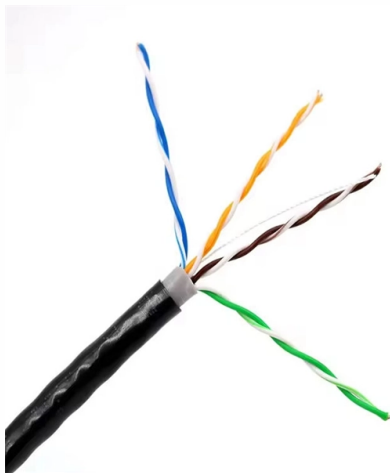


BERT Error Rate Analyzer Keysight



Overview

J-BERT M8041A High-Performance BERT Generator-Analyzer-Clock Module The M8041A is an instrument module that can be installed into the M9505A 5-slot AXIe Chassis.

Key Takeaways

The Keysight Technologies M8040A-64G-GEN is a highly integrated BERT (Bit Error Rate Tester) for physical layer characterization and compliance testing. With support for PAM-4 and NRZ signals and data rates up to 64 GBaud (corresponds to 128 Gbit/s) it covers all flavors of 200 and. See our online selection with free same day shipping Saved carts can be accessed from the Cart Summary Emulate every part of your data center infrastructure. Use 25+ X-Series applications to analyze, demodulate, and troubleshoot signals across wireless. Keysight Bit Error Ratio Testers (BERT) information including price quotes, manuals, application notes, reviews, videos, forums, and more. Bit Error Rate Test Sets (BERTS) are used to test many high-speed digital interfaces such as - QPI, FB-DIMM, PCI Express®, SATA/ SAS USB, Thunderbolt, Display Port, HDMI, MHL, MIPI, UHS-II, Fiber Channel, XAUI/10GB Ethernet, etc. TRS-RenTelco offers a wide variety of BERTS to Rent, Lease, or Buy. Page 2 ALL WARRANTIES, EITHER EXPRESS OR its standard commercial license, which is met. IMPLIED WITH REGARD TO THIS MANUAL embodied in its End.



Article Content

Keysight J-BERT M8020A High-Performance BERT and M8030A Multi-Channel BERT

J-BERT M8041A High-Performance BERT Generator-Analyzer-Clock Module The M8041A is an instrument module that can be installed into the M9505A 5-slot AXIe Chassis.

What is a Bit Error Ratio Tester (BERT)?

Find us at Keysight's M8040A BERT is designed for R&D and test engineers who characterize chips, devices, transceiver modules, and sub-components/boards/

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This document is for preliminary research. Verify specifications against current standards, product documentation and project requirements.

